

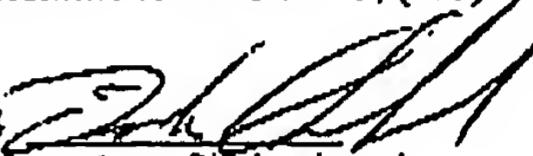
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Liu
 Serial No.: 10/616,201
 Filed: July 8, 2003
 For: CIRCUIT FOR OPTIMIZING
 DIODE ZENER BIAS
 CURRENT
 Group Art Unit: Unassigned
 Examiner: Unassigned
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**CERTIFICATE OF MAILING/TRANSMISSION
(37 C.F.R. § 1.8A)**

I hereby certify that this correspondence is, on the date shown below, being:
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October 13, 2004


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